

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10026470	12/27/2001	438	357	2817	35 NOLSAWMA

**APPLICANTS: Tam Derek; Cheng Jasmine; Song Jungwoo; Hayashi Takayuki;

**CONTINUING DATA VERIFIED:

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** FOREIGN APPLICATIONS VERIFIED:

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	ATTORNEY DOCKET NO
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no			1875.1350000
35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no			
Verified and Acknowledged Examiners's initials			
TITLE : Thick oxide P-gate NMOS capacitor for use in a phase-locked loop circuit and method of making same			

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436 (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
Assistant Examiner		Total Claims	Print Claim for O.G.
		DRAWING	
Primary Examiner		Sheets Drwg.	Figs. Drwg.
		Print Fig.	
PREPARED FOR ISSUE		Application Examiner	
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